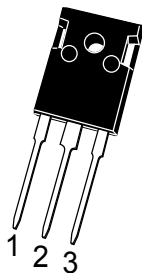


■ Productor Character

- Multilayer Metal -Silicon Potential Structure.
- Low Leakage Current.
- High Current Capability, High Efficiency.
- High Junction Temperature Capability.
- RoHS Product.
- Compliance with EU REACH.

■ Summarize

- EBR40L200PT Device optimized for ultra-low forward voltage drop to maximize efficiency in Power Supply applications.

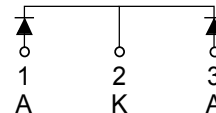


Package:TO-247AB
EBR40L200PT
EBR40L200PT-HF
HF=Halogen Free

■ Primary Use

- Low Voltage High Frequency Switching Power Supply.
- Low Voltage High Frequency Invers Circuit.
- Low Voltage Continued Circuit and Protection Circuit.

■ Equivalent Circuit



Absolute Maximum Ratings(Ta=25°C unless otherwise noted)

Item		Symbol	Data	Unit
Maximal Inverted Repetitive Peak Voltage		V_{RRM}	200	V
Average Rectified Forward Current(Rated VR-20Khz Square Wave)-50% duty cycle	Per Device	I_{FAV}	40	A
	Per Diode		20	
Forward Peak Surge Current(Rated Load 8.3 Half Mssine Wave-According to JEDEC Method)	Per Diode	I_{FSM}	360	A
Operating Junction Temperature		T_J	-40~175	°C
Storage Temperature		T_{STG}	-40~175	°C
Typical Thermal Resistance(per leg)	Package=TO-247AB	$R_{\theta JC}$	0.5	°C/W

Electricity Character Per Diode(Ta=25°C unless otherwise noted)

Item	Test Condition	Value(min)	Value(typ)	Value(max)	Unit
V_B	Tj=25°C,IR=1mA	200	—	—	V
I_R	Tj=25°C,VR=200V	—	—	10	μA
	Tj=125°C,VR=200V	—	—	5	mA
V_F	Tj=25°C,IF=20A	—	0.87	0.92	V
	Tj=125°C,IF=20A	—	—	0.8	V
C_J	Tj=25°C,VR=4V,f=1MHz	—	730	—	pF



Typical Characteristics Curves

Fig1. Typical Forward Current and Forward Voltage

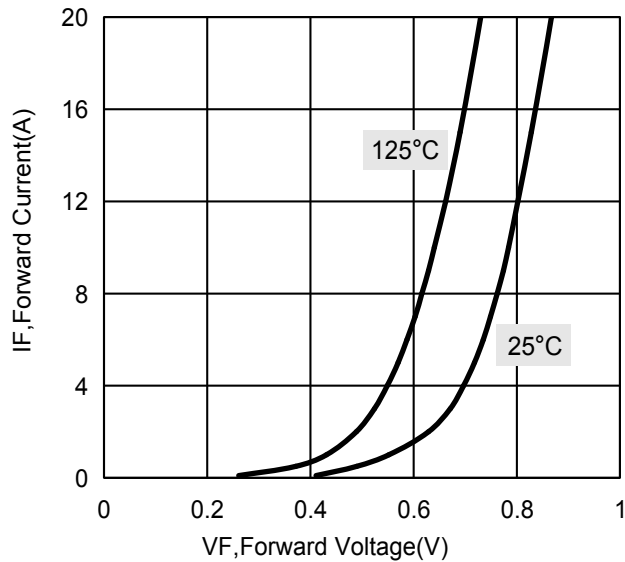


Fig2. Typical Current Derating Curve, Per Element

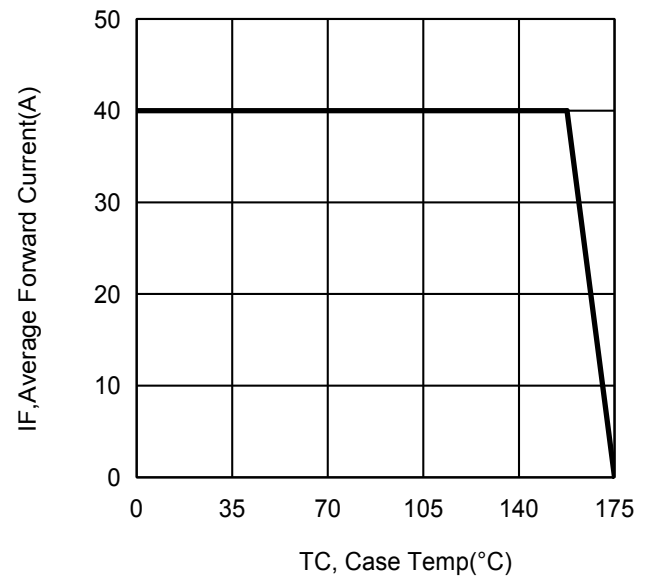


Fig3. Typical Reverse Current and Reverse Voltage

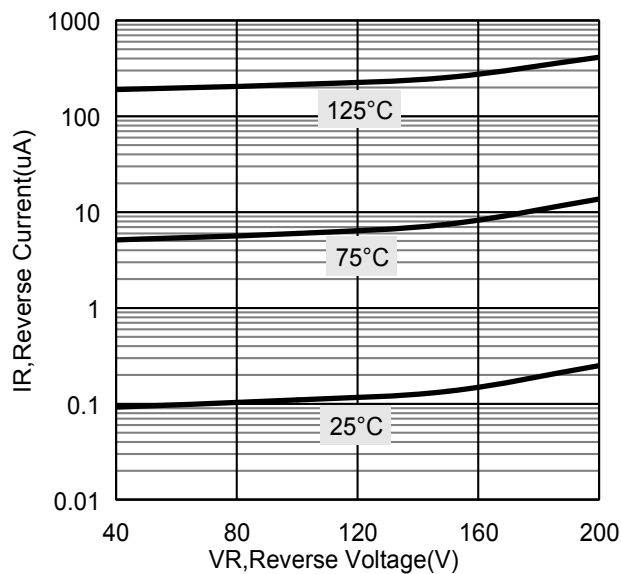
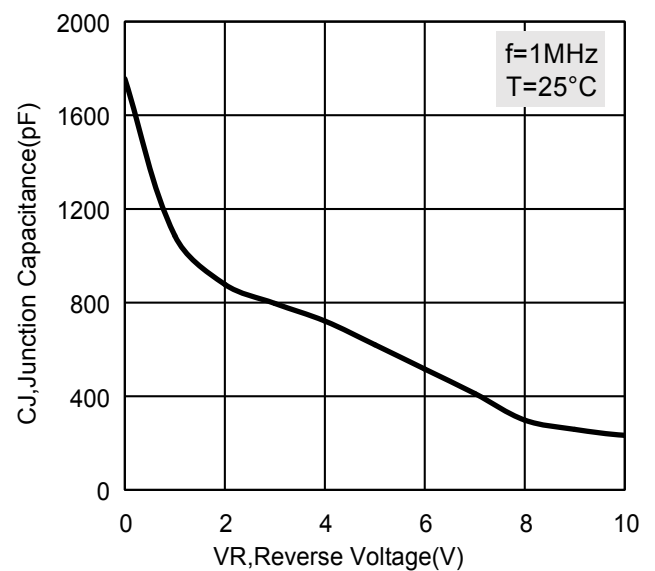
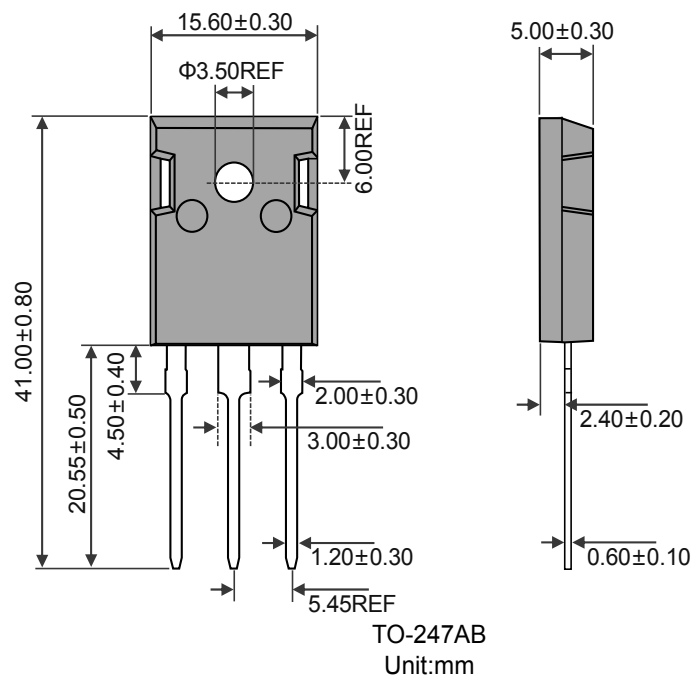


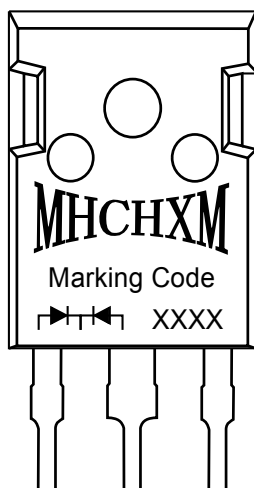
Fig4. Typical Junction Capacitance and Reverse Voltage



Package Outline Dimensions



Marking Information



“MHCHXM”= Product Logo
“Marking Code”= The Following
“XXXX”= Date Code Marking

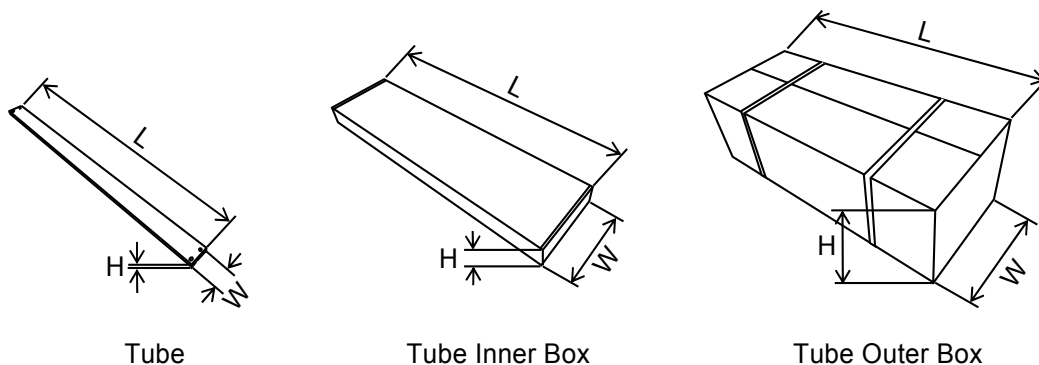
Marking Code	Part Number
EBR40L200PT	EBR40L200PT
	EBR40L200PT-HF



Packing Information

Packaging	Part Number	Quantity(pcs)	Size(mm)
Tube	Tube	30	L495×W46×H7
	Inner Box	360	L520×W115×H58
	Outer Box	1800	L540×W320×H135

Packaging:Tube



Notes

Lead Forming

1. During lead frame bending, the lead frame should be bent at a distance more than 3mm from bottom of the epoxy. And the bending degree should not exceed 90°.

Note: The lead frame must be secured and do not touch the epoxy before bending to avoid damage to the transistor. In addition, when using a mold for a large number of lead molding, the structure of the fixed lead must be set, and it should be noted that the lead pressure rod structure cannot exert pressure on the epoxy resin body.

2. Do not bend the lead repeatedly. Do not bend the lead outward



Heat sink mounting

For power devices, in order to reduce junction temperature, heat dissipation blocks are usually used to disperse heat to the outside, and semiconductor power devices installed on the heat dissipation blocks can effectively dissipate heat without losing the reliability of the semiconductor, so the following matters should be noted when using:

1. Pay attention to the selection of silicone cream

In order to improve the thermal conductivity and heat dissipation effect of the device and the heat dissipation block, generally apply a thin layer of silicone grease evenly on the contact surface of the device and the heat dissipation block. Choose a silicone grease with low oil separation degree. Do not overapply it, otherwise it will attach too much stress to the resin.

2. Optimum torque is required

When using the fastening torque, pay attention not to use too much torque, so as not to damage the epoxy resin body, pay attention to the smooth cooling block body, no file chips and other foreign bodies between the transistor and the cooling block, pay attention to the selection of screws, nuts, gaskets and washers, so as not to cause damage to the transistor due to improper selection.

Soldering

1. Pay special attention to welding. When welding, the distance between the solder joint and the epoxy ball should be greater than 3mm, and it is recommended to weld it outside the tie rod base.

2. Avoid applying any pressure to the lead frame while the transistor is at high temperatures, especially when welding. Dip welding and manual welding should not be done more than once

Notes:

For specific precautions, please refer to our company's relevant technical documents or visit our official website at <http://www.jshxm.com>



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